

Membrane-Templated Flexible Semiconductor Package

Ian Harvey Arellano

Central Engineering & Development, Backend Manufacturing and Technology
 STMicroelectronics, Inc., 9 Mountain Drive, LISP II, Calamba 4027 Laguna, Philippines

Abstract— Membrane-templated deformable carrier and elastomeric encapsulant enable flexible semiconductor package in support of the demands of wearables and flexible electronic gadgets. The selectively pore-filled membrane, with metal particle suspension, is mechanically reinforced by a thin elastomeric material layer to protect the newly formed leads, and to give structure to withstand package assembly. Package assembly is achieved by attaching a silicon die via a die attach process, electrically connecting the silicon to the carrier via wirebonding, and encapsulating the package using an elastomeric material via elastomeric molding process. The elastomeric support is then peeled off, exposing the terminal surface of the leads. The deformable carrier and the elastomeric encapsulant ensure package flexibility and conformity to distortion

Keywords— Flexible, deformable, metal particle, elastomer, wearable.

I. INTRODUCTION

Wearable electronics require flexible materials and ultrathin packages able to withstand multi-axes distortion. The use of conventional materials, like thick carriers and stiff thermoplastics, limit the success of producing highly conformable packages. New materials and innovative processes are necessary to realize the industry shift towards flexible semiconductor packages and electronic devices.

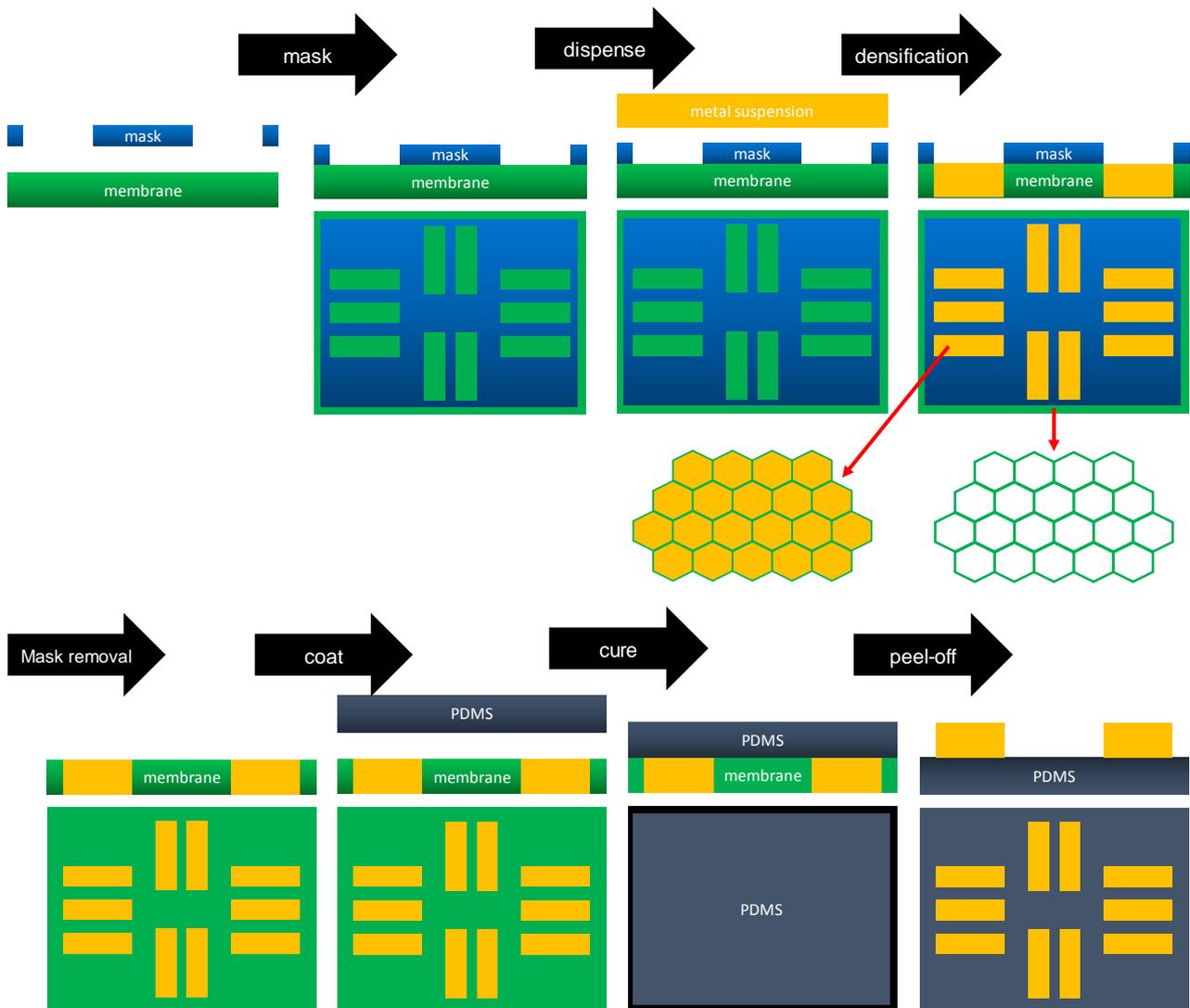


Fig. 1. Carrier assembly process flow.

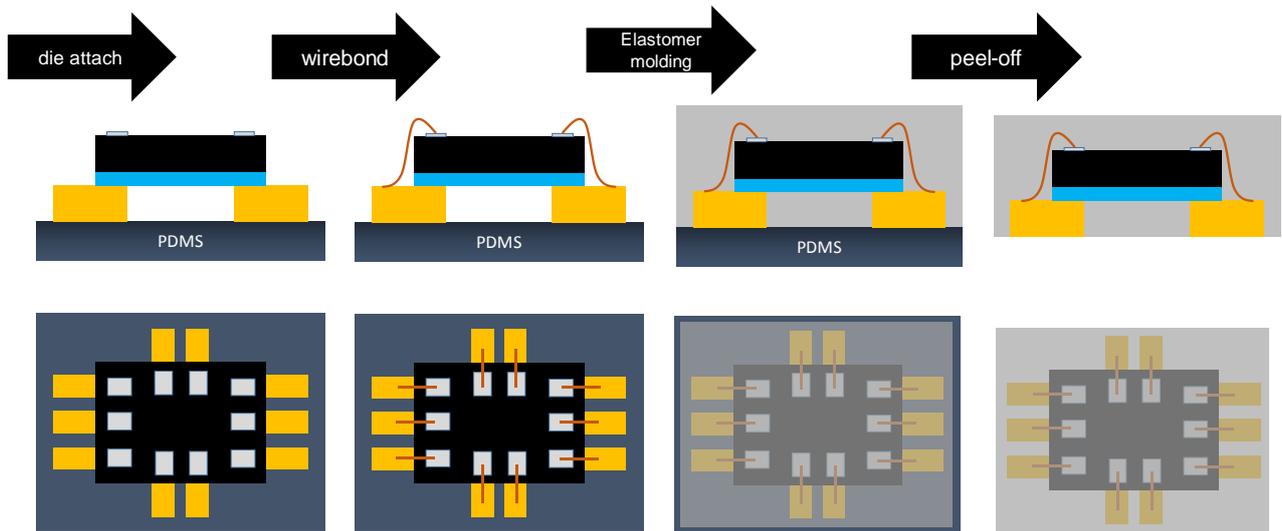


Fig. 2. Package assembly process flow.

Conventional semiconductor packages are assembled with a set of materials and processes resulting in products with minimal tolerance to distortion and bending. Delamination is a common failure mode encountered when conventional packages are subjected to distortion and bending. This failure mode can lead to package failure at time zero and during field application. To address the demands of wearable electronics and foldable display/devices, ultrathin conventional packages are assembled. The manufacturing of ultrathin packages requires a new set of materials with better tolerance to thermomechanical stresses associated with bending and distortion.

II. DESIGN AND PROCESS SOLUTION

The solution assembles semiconductor packages using a deformable modified carrier and an elastomeric encapsulant. The modified carrier is built (Fig. 1) using a membrane sheet/film as template, wherein the pores are selectively filled with a conductive material from a suspension of metallic particles, forming the leads. The lead footprint is achieved using a mask. The metallic particles from the suspension, conforming to the mask pattern, are densified via suction filtration. A thin elastomeric material (e.g. polydimethylsiloxane) layer to protect the newly formed leads, and to give structure to withstand package assembly, mechanically reinforces the selectively pore-filled membrane. The metallic particles are fused during the curing of the elastomeric support, forming a solid wirebondable surface. The unfilled portions of the membrane is peeled off from the assembly, leaving the metallic pattern attached to the elastomeric support, completing the assembly of the deformable carrier. Package assembly (Fig. 2) is achieved by attaching a silicon die via a die attach process, electrically connecting the silicon to the carrier via wirebonding, and encapsulating the package using an elastomeric material via elastomeric molding process. The elastomeric support prevents any mechanical (scratches, chipping, etc.) and chemical damage (corrosion) during assembly. The elastomeric support is then peeled off, exposing the terminal

surface of the leads. The deformable carrier and elastomeric encapsulant ensure package flexibility and conformation to distortion. The resulting package cross-sectional and top views are shown in Fig. 3.

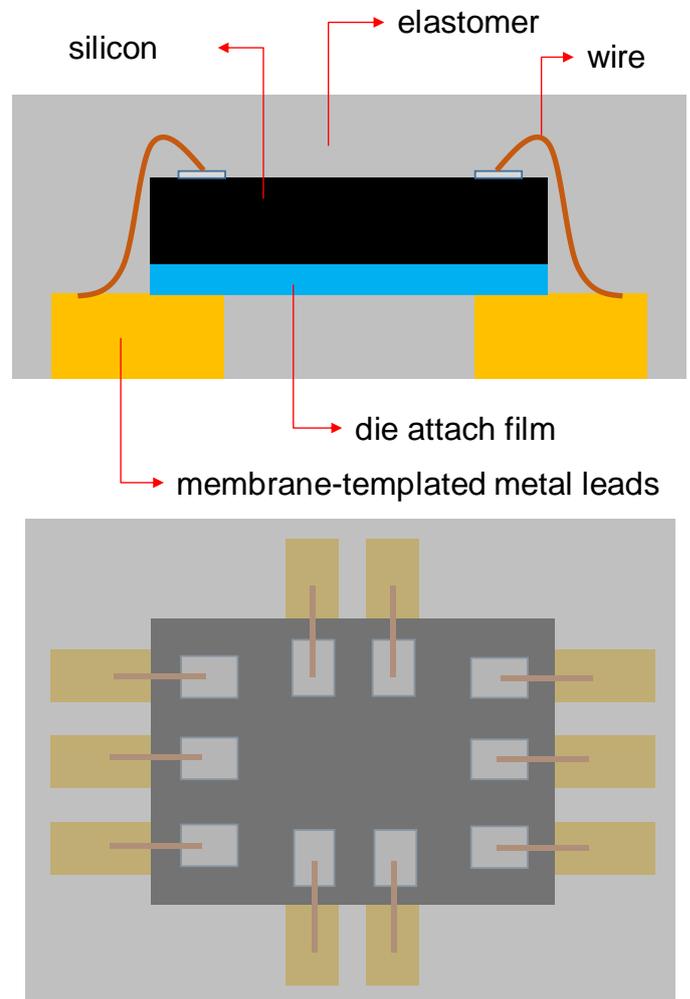


Fig. 3. Package cross-sectional (top) and top (bottom) views.

III. CONCLUSION

The novel solution enables flexible semiconductor package by using deformable membrane-templated carrier and elastomeric encapsulant. The membrane-templated carrier provides higher flexibility and conformity to distortion arising from its deformable pore structure & thin dimension. The elastomeric support prevents any mechanical (scratches, chipping, *etc.*) and/or chemical damage (corrosion) during assembly. The elastomeric encapsulant provides flexibility towards bending and distortion.

REFERENCES

- [1] Naghdi, S., Rhee, K.Y., Hui, D., Park, S.J. A review of conductive metal nanomaterials as conductive, transparent, and flexible coatings, thin films, and conductive fillers: Different deposition methods and applications. *Coatings* 2018, 8, 278 (27 pp).
- [2] Schlemmer, W., Fischer, W., Zankel, A., Vukušić, T., Filipič, G., Jurov, A., Blažeka, D., Goessler, W., Bauer, W., Spirk, S., Krstulović, N. Green Procedure to Manufacture Nanoparticle-Decorated Paper Substrates. *Materials* 2018, 11, 2412 (11 pp).
- [3] Shipway, A.N., Katz, E., Willner, I. Nanoparticle arrays on surfaces for electronic, optical, and sensor applications. *ChemPhysChem* 2000, 1, 18-52.
- [4] Contini, C., Schneemilch, M., Gaisford, S., Quirke, N. Nanoparticle-membrane interactions *J. Exp. Nanosci.*, 2018, 13, 62-81.